Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	14978	basic adj unit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/27 15:41
L2	171585	(layout adj layer) or (metal adj layer)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/27 15:42
L3	3	((layout adj layer) or (metal adj layer)) with program\$4 with basic with clock\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/27 15:45
L4	7	((layout adj layer) or (metal adj layer)) with program\$4 with clock\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/27 15:51
L5	0	clock\$3 adj4 circuit with basic adj unit with program\$4 with isolat\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/27 15:53
L6	3	clock\$3 adj4 circuit with basic adj unit with non-clock\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/27 15:53
S1	50416	fabricat\$3 adj3 circuit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/17 15:27
S2	26969	semiconductor adj4 body	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/17 15:27

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S3	18552	(basic adj unit) or (basic adj cell)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/17 15:30
S4	3470	(synchronous or clock\$3 adj3 circuit) and (non-synchronous or non-clock\$3 adj3 circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/17 15:32
S5	34	program\$4 near metal near layer	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/17 16:12
S6	13	S3 and S4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/17 15:33
S7	.3	S5 and S6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ÓN	2006/05/17 15:48
S8	3	"6902957"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR .	ON	2006/05/17 15:56
S9	59552	(transistor adj5 series) or (transistor adj4 cascad\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	. OR	ON	2006/05/17 16:13
S10	442	S3 and S9	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/17 15:57

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S11	1	S4 and S10	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/17 15:58
S12	1	S5 and S10	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/17 15:59
S13		S6 and S9	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR .	ON	2006/05/17 15:59
S14	19	S2 and S10	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/17 16:05
S15	19	unit and S14	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/17 16:09
S16	1	S5 and S15	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/17 16:10
S17	8	S5 and S9	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/17 16:10
S18	3	S4 and S5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR ·	ON	2006/05/17 16:12

S19	114	((transistor adj5 series) or (transistor adj4 cascad\$3)) near unit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/17 16:20
S20	. 0	S4 and S19	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/17 16:14
S21	0	(transistor adj5 series adj4 cascad\$3) near unit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/17 16:17
S22	. 75	S4 and S9	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/17 16:17
S23	. 1	S2 and S22	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/17 16:17
S24	0	((transistor adj5 series) or (transistor adj4 cascad\$3)) near unit near basic	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/17 16:21
S25	3619	(716/1,8,11,16-17).ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/17 16:22
S26	2597	(438/128-129).ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/17 16:23

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S27	1	basis near (transistor adj5 series adj4 cascad\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR .	ON	2006/05/24 10:01
S28	0	unit near (transistor adj5 series adj4 cascad\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/24 10:00
S29		basi\$ near (transistor adj5 series adj4 cascad\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/24 10:02
S30	16014	basi\$ adj unit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/24 10:01
S31	13043	basi\$ adj2 cell	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/24 10:03
S32	40	transistor adj5 series adj4 cascad\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/24 10:03
S33	0	S30 and S32	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON .	2006/05/24 10:02
S34		S31 and S32	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/24 10:02

S35		basi? near (transistor adj5 series adj4 cascad\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/24 10:02
S36	28689	S30 or S31	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/24 10:03
S37	77	transistor adj7 series adj7 cascad\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/24 10:13
S38	0	S36 and S37	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/24 10:04
S39	89255	clock\$3 adj3 circuit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/24 10:04
S40	30	non?clock\$3 adj3 circuit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/24 10:05
S41	30	S39 and S40	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	·OR	ON	2006/05/24 10:05
S42	0	S32 and S41	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR .	ON	2006/05/24 10:05

S43 0 S37 and S41	US-PGPUB; C USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR Of	N 2006/05/24 10:12
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Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	(basic and unit and clocked and circuit and non-clocked and identical and program\$4 and semiconductor adj layout and layer).clm.	US-PGPUB; USPAT	OR	ON	2006/11/27 15:57
L2	1	(basic and unit and clocked and circuit and non-clocked and identical and program\$4 and semiconductor and layout and layer).clm.	US-PGPUB; USPAT	OR	ON	2006/11/27 15:57

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